



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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2183

In re Patent Application of

SWAINE

Atty. Ref.: 550-235

Serial No. 09/876,220

Group: 2183

Filed: June 8, 2001

Examiner:

For: APPARATUS AND METHOD FOR STORING INSTRUCTION
SET INFORMATION

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July 28, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

As suggested by 37 C.F.R. 1.97, the undersigned attorney brings to the attention of the Patent and Trademark Office the references listed on the attached form PTO-1449. This is not to be construed as a representation that a search has been made or that no better prior art exists, or that a reference is relevant merely because cited.

Many of these references were cited in the parent application serial no. 09/773,387, (attorney docket 550-205), filed February 1, 2001, (a copy of which is enclosed), from which the instant application claims priority under 35 U.S.C. §120.


The Examiner's attention is also directed to related U.S. application serial number 09/792,643, filed on February 26, 2001, (a copy of which is enclosed). The '643 application is a continuation-in-part of the '387 application. The instant application claims priority from the '643 application. Copies of references cited in the '643 have been included.

The Examiner is requested to initial the attached form PTO-1449 and to return a copy of the initialed document to the undersigned as an indication that the attached references have been considered and made of record.

Respectfully submitted,

NIXON & VANDERHYE P.C.

By:



John R. Lastova
Reg. No. 33,149

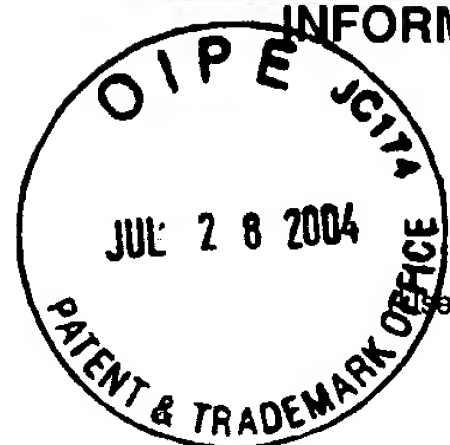
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**INFORMATION DISCLOSURE
CITATION**

ATTY. DOCKET NO.

SERIAL NO.

550-235

09/876,220

APPLICANT

SWAINE

FILING DATE

GROUP

June 8, 2001

2183

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,802,273	9/1998	Levine et al.			
	4,590,550	5/1986	Eilert			
	4,821,178	4/1989	Levin et al.			
	5,151,981	9/1992	Westcott et al.			
	5,347,647	9/1994	Allt et al.			
	5,550,974	8/1996	Pennington et al.			
	5,555,392	9/1996	Chaput et al.			
	5,923,872	7/1999	Chrysos et al.			
	5,978,742	11/1999	Pickerd			
	5,987,598	11/99	Levine			
	6,000,044	12/1999	Chrysos et al.			
	6,009,270	12/1999	Mann			
	6,052,802	4/2000	Zahir et al.			
	6,067,644	5/2000	Levine et al.			
	6,139,198	10/2000	Danforth et al.			
	6,415,378	7/2002	Davidson et al.			
	6,539,502	3/2003	Davidson et al.			
	6,574,727	6/2003	Davidson et al.			
	4,439,827	3/1984	Wilkes			

FOREIGN PATENT DOCUMENTS

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
0 465 765	1/1992	EP			
0 689 141	12/1995	EP			
0 919 919	6/1999	EP			

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

	ARM966E-S Technical Reference Manual, 12/1999, Arm Limited, Issue A, Chapter 1.
	Embedded Trace Macrocell Architecture Specification, 12/2002, Arm Limited, Issue 1, Chapters 1 and 2 and Appendix B.
	Uhlig et al., Trace-Driven Memory Simulation: A Survey, 6/1997, ACM Computing Surveys, Vol. 29, No. 2, pages 128-170.
	ARM IHI 0014 Revision C - Embedded Trace Macrocell Specification .
	ARM DDI 0157F - ETM9 (Rev. 1) Technical Reference Manual
	ARM DDI 0158D-ETM7 (Rev. 1) Technical Reference Manual.
	U.S. application serial number 09/773,387, filed February 1, 2001.
	ARM IHI 0014 Revision I-Embedded Trace Macrocell Spec.

*Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

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